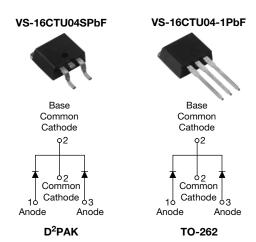


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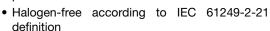
Ultrafast Rectifier, 2 x 8 A FRED Pt®

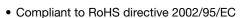


PRODUCT SUMMARY					
t _{rr} 60 ns					
I _{F(AV)}	2 x 8 A				
V_{R}	400 V				

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C





• AEC-Q101 qualified





DESCRIPTION/APPLICATIONS

Vishay HPP's FRED Pt^{\otimes} series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage		V_{RRM}		400	V	
Average rectified forward current	per leg			8		
Average rectified forward current total device		I _{F(AV)}	Rated V _R , T _C = 155 °C	16	_	
Non-repetitive peak surge current		I _{FSM}	T _C = 25 °C	100	Α	
Peak repetitive forward current		I _{FRM}	Rated V _R , square wave, 20 kHz, T _C = 155 °C	16		
Operating junction and storage temperatures		T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS PER LEG (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS MIN. TYP. MA			MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	400	-	-			
Cammand malka an	V _F	I _F = 8 A	-	1.19	1.3	V		
Forward voltage	VF	I _F = 8 A, T _J = 150 °C	-	0.94	1.0			
Reverse leakage current I _R		V _R = V _R rated	-	0.2	10			
		$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{ rated}$	-	20	500	μΑ		
Junction capacitance	C _T	V _R = 400 V		14	-	pF		
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8.0 -			nH			

Document Number: 94009 Revision: 11-Mar-10

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Ultrafast Rectifier, 2 x 8 A FRED Pt®



DYNAMIC RECOVERY CHARACTERISTICS PER LEG (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1.0 \text{ A, } dI_F/dt =$	-	35	60			
Reverse recovery time	t _{rr}	T _J = 25 °C		-	43	-	ns A nC	
		T _J = 125 °C		-	67	=		
Peak recovery current		T _J = 25 °C	$I_F = 8 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	2.8	-		
	IRRM	T _J = 125 °C		-	6.3	=		
Reverse recovery charge	0	T _J = 25 °C		-	60	=		
	Q _{rr}	T _J = 125 °C]	-	210	-		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}	Stg		-	175	°C	
Thermal resistance, junction to case per leg	R _{thJC}	R _{thJC}		1.8	2.0		
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	50	°C/W	
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased		0.5	-		
Maight			-	2.0	-	g	
Weight			-	0.07	-	oz.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)	
Marking daving		Case style D ² PAK	16CTU04S			•	
Marking device		Case style TO-262		16CT	U04-1		

For technical questions, contact: diodestech@vishay.com



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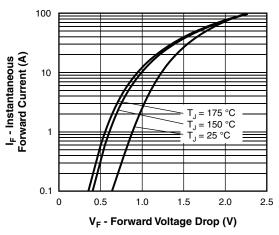


Fig. 1 - Typical Forward Voltage Drop Characteristics

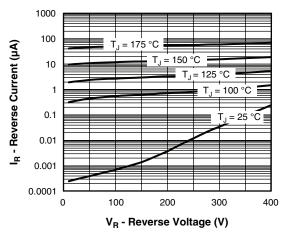


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

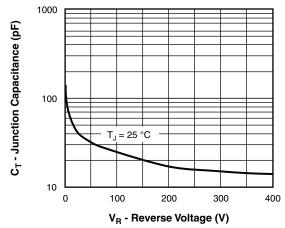


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

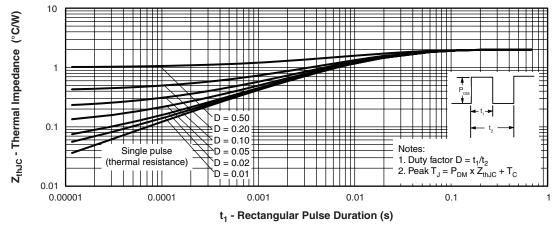


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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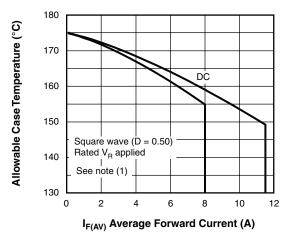


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

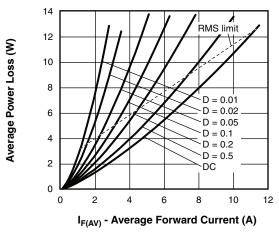


Fig. 6 - Forward Power Loss Characteristics

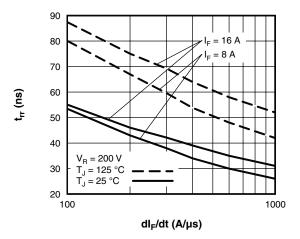


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

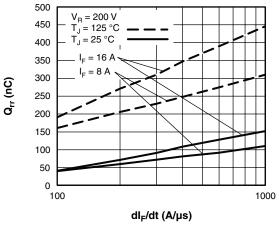


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$

Document Number: 94009 Revision: 11-Mar-10

4

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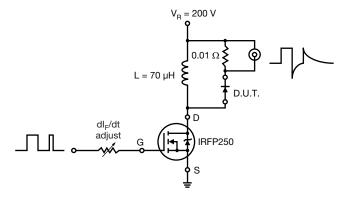
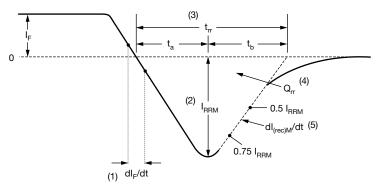


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code

VS-	16	С	Т	U	04	S	TRL	PbF
1	2	3	4	5	6	7	8	9

1 - HPP product suffix

2 - Current rating (16 A)

3 - C = Common cathode

T = TO-220, D²PAK

U = Ultrafast recovery

6 - Voltage rating (04 = 400 V)

7 - • $S = D^2PAK$

• -1 = TO-262

None = Tube (50 pieces)

• TRL = Tape and reel (left oriented, for D²PAK package)

• TRR = Tape and reel (right oriented, for D²PAK package)

9 - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS						
Dimensions <u>www.vishay.com/doc?95014</u>						
Part marking information	www.vishay.com/doc?95008					
Packaging information	www.vishay.com/doc?95032					

For technical questions, contact: diodestech@vishay.com

Document Number: 94009

Revision: 11-Mar-10



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